

REMARKS

Applicants respectfully traverse and request reconsideration.

Claims 1-21 stand rejected under 35 U.S.C. § 103(a) as allegedly being unpatentable over Hooper in view of U.S. Publication No. 20030083064 (Cooper). This is a new ground of rejection. It is admitted that Hooper does not teach claimed subject matter. It is alleged that Cooper teaches logic circuitry “operatively coupled to the memory and operative to perform a first more preferred SID acquisition sequence and then a second more preferred SID acquisition sequence that includes repeatedly attempting acquisition of the at least one more preferred stored SID element using a same frequency during the second more preferred SID acquisition sequence” (office action, page 3). The office action alleges that paragraph 0039 of Cooper teaches this subject matter. However, Applicants respectfully submit that the cited portion of Cooper is directed to a completely different operation from that claimed. It appears that the only mention in the cited portion of the Cooper reference with respect to SID acquisition actually confirms that the Cooper reference operates in a different manner from that claimed.

The cited portion of Cooper is actually directed not to a SID acquisition operation but instead is directed to an initial analog or digital system acquisition sequence as set forth also in paragraph 0038. As specifically stated by Cooper, what actually occurs is that all frequencies in an A frequency set or all frequencies in a B frequency set are scanned, if an analog system is attempting to be acquired as opposed to a digital system, and the strongest signal in the scanned frequencies of all frequencies is then demodulated. If the demodulation is successful, then a SID for that strongest demodulated signal is acquired. Once the SID is acquired for the strongest signal, only then is it determined “whether to provide service on the acquired system (as identified by the SID) based on, for example, whether the system is a preferred or negative system.” (Paragraph 0039). As such, Cooper actually teaches a different operation from that

claimed since it instead teaches first locking onto a strongest signal after scanning all frequencies which is a time consuming process and then only determining whether the SID is a preferred SID or a negative SID after it has already been acquired. There is no contemplation or discussion of any multiple, more preferred SID acquisition sequence operation that utilizes the same frequency to acquire the more preferred stored SID element during both the first and second more preferred SID acquisition sequence as set forth in the independent claims. Instead, Cooper teaches a single SID acquisition operation that is solely based on the strongest signal acquired after searching “all frequencies”. Since neither Hooper nor Cooper describe nor contemplate the claimed subject matter, Applicants respectfully submit that the claims are in condition for allowance.

The dependent claims add additional novel and non-obvious subject matter.

Applicants respectfully submit that the claims are in condition for allowance and respectfully request that a timely Notice of Allowance be issued in this case. The Examiner is invited to contact the below listed attorney if the Examiner believes that a telephone conference will advance the prosecution of this application.

Respectfully submitted,

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